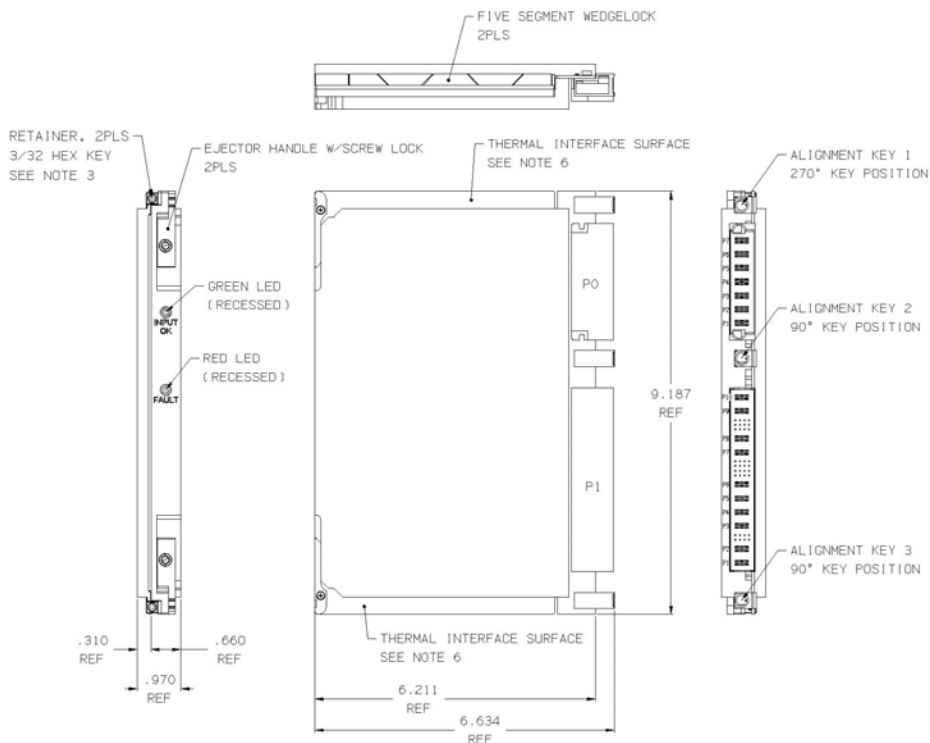


VPX-1500S-3P

P0 - AC/DC INPUT CONNECTOR TE CONNECTIVITY P/N 6450843-6	
PIN NO.	SIGNAL
P7	ØA
P6	ØB
P5	ØC
P4	NEUTRAL
P3	+360VDC OPTION
P2	-360VDC OPTION
P1	CHASSIS GND

P1 - DC OUTPUT CONNECTOR TE CONNECTIVITY P/N 6450849-6			
PIN NO.	SIGNAL	PIN NO.	SIGNAL
P10	+28V/53A	D5	SDA
P9	+28V/53A	A4	GA3*
A9	+28V SENSE	B4	GA2*
B9	+28V SENSE	C4	GA1*
C9	N/C	D4	GA0*
D9	N/C	A3	N/C
A8	+28V SENSE RTN	B3	N/C
B8	+28V SENSE RTN	C3	NED
C8	N/C	D3	NED RTN
D8	N/C	P6	N/C
A7	N/C	P5	N/C
B7	N/C	P4	N/C
C7	N/C	P3	N/C
D7	SIGNAL RTN	A2	VBAT
P8	POWER RTN	B2	FAIL*
P7	POWER RTN	C2	INHIBIT*
A6	N/C	D2	ENABLE*
B6	N/C	A1	N/C
C6	N/C	B1	N/C
D6	SYSRESET*	C1	N/C
A5	GAP*	D1	N/C
B5	GA4*	P2	+3.3V_AUX/4A
C5	SCLK	P1	POWER RTN



Nominal Input Voltage	115/200 Vac 3-Phase, 6A nominal.
Frequency	47-63Hz, 400Hz.
Operational Input Voltage Range	The steady-state voltage average for the three phases is within the limits of MIL-STD-704 A.
Input Load Balance	Current loading for any phase does not exceed the average of the currents in all 3 phases by more than 5%.
Inrush Current	Less than 4 msec. 60 amperes @ 200 Vac.
Fusing	(3 X 8 Ampere)/250 Vac, Very fast acting. Internal ceramic body fuses.
Hold up time	20msec minimum after loss of AC Input at full load and any input.
Efficiency	90% typical.
Turn on time	1 sec max. from power up.
Line and Load Regulation	±2% over AC input range and 0 to 100% load change.
Minimum Load	No minimum load required.
Ripple & Noise	Through 20MHz 0.5% max. or 50mv whichever is greater for both outputs, peak to peak, with coaxial probe and 0.1uF/10uF capacitors at the connector.
Transient Response	Output maximum excursion of ± 5% for 25% load step. Recovery less than 500 µsec.
Overshoot	No turn-on or turn-off overshoot.
Output Isolation	Isolated from chassis ground, 100Vdc.
Input/Output Isolation	1500Vdc from input to both chassis/outputs.
Reverse Voltage	Protected against reverse voltage to supply current rating.
Overvoltage Protection	Shutdown at 130% of nominal Vout. Recycle input power to reset.
Overtemperature Protection	Unit shuts down if overheated. Recycle input.
Leakage Current	3.5mA max at 200Vac.
Current Limiting	All outputs protected with current limit. Automatic recovery when overload or short is removed.
Paralleling	Two or more supplies can be operated in parallel and will share load current within 10% of each other.

Redundant	Full power N+1 redundant with integral Oring FET's/Diodes.
Remote Sense	Compensates for up to 0.5V total distribution voltage drop on the +28V output.
Enable*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
INHIBIT*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
SYSRESET*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
FAIL*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
NED	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
VBAT	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
Geographical Addressing	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
Protocol (I²C)	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.
Indicators	Green LED indicating Input OK, Red LED indicating a power supply fault.
Cooling	Conduction cooled via wedge lock retainers.
Operating Temperature	-40°C to 85°C (at wedge lock edge) 1500W
Stability	All outputs 0.1% for 8 hrs. after 30 minute warm-up.
Humidity	Up to 95% non-condensing.
Storage Temperature	-55°C to 105°C.
Connectors	VITA 62 compliant
Size	6U x 5HP (1") x 160mm Weight: 3.5 lbs.
EMC	Designed to meet Mil-Std-461F (excluding CE101) with SPI's external filter, Top Assembly 25940, or equivalent.
Common Options	Conformal coating with Paylene & special output configurations. Consult factory for more details on a tailored solution which meets your requirements.