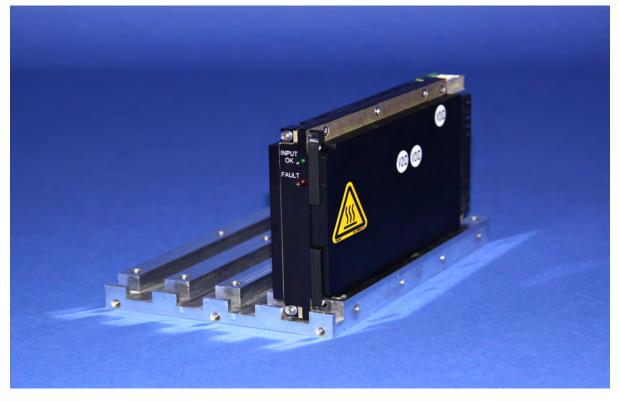
VPX-700S-DC-270-28V

700 Watts Conduction Cooled OpenVPX VITA 62 Compliant



KEY FEATURES:

- 700 Watts in a 3U x 5 HP (1") x 160mm Modular Design
- 270Vdc Input per Mil-Std-704 Versions E & F
- VITA 62 Outputs; +28V/25A, Aux_+3.3V/2A
- N+1 Redundant with Internal Oring FET's
- VITA 62 Card Guide Style Conduction Cooled
- 1 Inch Pitch Form Factor with Wedge Lock Retainers
- Side Covers Support Two-Level Military Maintenance Requirements
- Ruggedized Mechanical Design
- One Year Warranty
- Greater than 150,000 Hrs MTBF
- Proudly Made in U.S.A.



3601 Veterans Highway Ronkonkoma, New York 11779 Tel: 631-981-7231 Fax: 631-981-7266



VPX-700S-DC-270-28V

	PUT/OUTPUT CONNEL CONNECT[V]TY P/		-7						
PIN NO.	SIGNAL	PIN NO.	SIGNAL						
P1	-DC_1N	A6	N/C						
P2	+DC_IN	B6	N/C	RETANER, 2PLS –			∠FIVE SEGMENT WEDG 2PLS	ELUUK	
LP1	CHASSIS GND	C6	N/C	3/32 HEX KEY	- EJECTOR	HANDLE	ZELO		IGNMENT KEY 1
A1	GA2*	D6	SYSRESET*	SEE NOTE 3	/ 2PLS			31	5° KEY POSITION
B1	N/C	A7	N/C		/				
C1	N/C	B7	N/C	Č.	ž t	e			
D1	N/C	C7	N/C	GREEN LED -	•	¥			
A2	VBAT	D7	SIGNAL_RTN	(RECESSED)					
B2	FA[L*	A8	+28V SENSE			4			
C2	INHIB1T*	B8	N/C	2 PALT	3.937			PO	
D2	ENABLE*	C8	N/C	RED LED -	REF			FU	
AЗ	N/C	D8	+28V SENSE_RTN	(RECESSED)		1			
ВЗ	N/C	РЭ	N/C						
СЗ	NED	P4	+28V RTN			<u> </u>			
D3	NED_RETURN	P5	+28V RTN	l di	∃ ±	- P		┶╌┦	
A4	3.3V_AUX	LP2	+28V/25A				6.211		
B4	3.3V_AUX 2A	P6	+28V/25A			-	REF		
C4	3.3V_AUX 24			.970			6.634	_	
D4	3.3V_AUX			REF			REF	-1	
A5	GA0*								ALIGNMENT KEY 2 -
B5	GA1*							_	90° KEY POSITION
С5	SCLK								
05	SDA							ـــــــــــــــــــــــــــــــــــــــ	

Nominal Input Voltage		270 Vdc, 2.9A.	Redundant	Full power N+1 redundant with integral Oring FET's.		
	Operational Input Voltage Range	240-290 Vdc, with input transient protection to 200 & 370 Vdc for 50 ms exceeding limits per MIL-STD- 704E/F.	Remote Sense	Compensates for up to 0.5V total distribution ve drop on the +28V output.		
	Inrush Current	Less than 4 msec, 60 amperes @ 270 Vdc.	Enable*	VITA 62 compliant. Reference SPI's VPX Signal of for more details. VITA 62 compliant. Reference SPI's VPX Signal of for more details.		
	Reverse Input Protection	Reverse input protection to rated DC voltage.	INHIBIT*			
	Fusing	6.3 Ampere, 400 Vdc, Internal ceramic body fuse.	SYSRESET*	VITA 62 compliant. Reference SPI's VPX Signal of for more details.		
	Hold up time	20msec minimum after loss of DC input at 500W load.				
	Efficiency	90% typical	FAIL*	VITA 62 compliant. Reference SPI's VPX Signal of for more details.		
	Turn on time	1 sec max. from power up.	NED	VITA 62 compliant. Reference SPI's VPX Signal c		
	Line and Load	±2% over DC input range and		for more details.		
Regulation Minimum Load		0 to 100% load change. No minimum load required.	VBAT	VITA 62 compliant. Reference SPI's VPX Signal c for more details.		
	Ripple & Noise	Through 20MHz 0.5% max. or 50mv whichever is greater for both outputs, peak to peak, with coaxial	Geographical Addressing	VITA 62 compliant. Reference SPI's VPX Signal of for more details.		
		probe and 0.1uF/10uF capacitors at the connector.	Protocol (I ² C)	VITA 62 compliant. Reference SPI's VPX Signal of		
	Transient Response	Output maximum excursion of \pm 5% for 25% load step. Recovery less than 500 µsec.		for more details.		
Overshoot		No turn-on or turn-off overshoot.	Indicators	Green LED indicating Input OK, Red LED indica a power supply fault.		
	Output Isolation	Isolated from chassis ground, 100 Vdc.	Cooling	Conduction cooled via wedge lock retainers.		
	Input/Output Isolation	1500Vdc from input to both chassis/outputs.	Operating Temperature	-40°C to 71°C (at wedge lock edge) 700W. Up to 85°C at 650W.		
	Reverse Voltage	Protected against reverse voltage to supply	Stability	All outputs 0.1% for 8 hrs. after 30 minute warr		
	-	current rating.	Humidity	Up to 95% non-condensing.		
	Overvoltage	Shutdown at 130% of nominal Vout.	Storage Temperature	-55°C to 105°C.		
	Protection	Recycle input power to reset.	Connectors	VITA 62 compliant		
	Overtemperature Protection	Unit shuts down if overheated. Recycle input.	Size	3U x 5HP (1") x 160mm Weight: 1.75 lbs.		
	Current Limiting	All outputs protected with current limit. Automatic recovery when overload or short is removed.	EMC	Designed to meet Mil-Std-461F with SPI's exte Top Assembly 25880, or equivalent.		
Paralleling		Two or more supplies can be operated in parallel and will share load current within ±10% of each other.	Common Options	Conformal coating with Paylene & special output configurations. Consult factory for more details tailored solution which meets your requirements		

	FET's.					
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HIBIT*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.					
/SRESET*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.					
NL*	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.					
ED	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.					
BAT	VITA 62 compliant. Reference SPI's VPX Signal data sheet for more details.					
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